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Date Mailed: February 6, 2008

	FORM 1449* INFORMATION DISCLOSURE STATEMENT	Docket Number: Application Number:			
		10/553 860			
	IN AN APPLICATION	Applicant: NANAI et al.			
	(Use several sheets if necessary)	Filing Date: October 20, 2005 Group Art Unit: 2818			

			U.S. PATENT DOCUM	ENTS			
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING IF APPRO	
							
							
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	JP 11-251259	9/17/1999	Japan			Abstract	ļ
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	ОТНЕ	R DOCUMEN	TS (Including Author, Tit	le. Date, Pertiner	it Pages, Etc.)		
	Avouris et al. "Carbon Nanotube Transistors and Logic Circuits," PHYSICA B, October 2002, vol. 323, Issues 1-4, pp. 6-14						
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53148 Customer Number

EXAMINER	DATE CONSIDERED				
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.					

*Substitute Disclosure Statement Form (PTO-1449)

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